



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/699,221 Confirmation No.: 2799  
First Named Inventor: Constantin Bulucea Filing Date: 31 October 2003  
Group Art Unit: 2814 Examiner: Farahani, D.  
Atty. Docket No.: NS-5127-1D US  
Title: Design and Operation of Gate-enhanced Junction Varactor With Gradual Capacitance Variation  
Assignee: National Semiconductor Corporation

Mountain View, California  
3 April 2006

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**AMENDMENT TO DRAWINGS UNDER 37 CFR 1.312**

Sir:

Under the provisions of 37 CFR 1.312, the drawings of the above patent application should be amended as follows:

In Fig. 15, reference symbol "112C" should be changed to "112LC".

In Fig. 17, reference symbols "126A", "126B", and "126C" should be respectively changed to "126A, N<sub>BA</sub>", "126B, N<sub>BB</sub>", and "126C, N<sub>BC</sub>".

Enclosed is a copy of the relevant drawing sheets in which the foregoing changes are indicated in red.

In addition, new Figs. 14.1 and 14.2 should be added as shown on the accompanying informal drawing sheet containing Figs. 14.1 and 14.2.

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